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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. KATSURA, et al

Serial No.: 09/536,646

Filing Date: March 28, 2000

For: GRAPHIC PROCESSING APPARATUS UTILIZING IMPROVED
DATA TRANSFER TO REDUCE MEMORY SIZE

Art Unit: 2772-2671

Examiner: Not yet assigned

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

June 1, 2000

Sir:

IN THE CLAIMS

[Please amend the claims as follows:]

1/2. (Amended) A graphic processing apparatus comprising:

9 a memory which stores graphic data;

a data processor which executes a predetermined
graphic processing to generate graphic data to be stored in
said memory;

a memory controller which controls data transfer
between said memory and said data processor in accordance with
a request from said data processor;

a digital to analog converter (DAC), connected to
said memory controller, which outputs said graphic data read
out from said memory;

a first bus, having m (wherein m is an integer) bits
width, connected between said memory and said memory